---VHDL & Vivado

vhdl -> https://www.youtube.com/watch?v=sfW3cNAhT60

vhdl con Xilinx -> https://www.youtube.com/watch?v=2qx7Xef2LGE

vhdl on vivado -> https://www.youtube.com/watch?v=ghgqA9imz9I

vhdl on Vivado -> <https://www.youtube.com/watch?v=ShjXQdKdxsE>

VHDL Learn By Example -> <http://esd.cs.ucr.edu/labs/tutorial/>

VHDL esempi registri e contatori → https://www.xilinx.com/support/documentation/university/Vivado-Teaching/HDL-Design/2015x/VHDL/docs-pdf/lab6.pdf

Esempi preinstallati della suite vivado

VHDL Tutorial -> https://www.youtube.com/watch?v=zm-RA6BsYmc

---git

Tutorial git -> <https://www.atlassian.com/git/tutorials>

---Tutorial su Vivado

-Interfaccia e operazioni base → <https://www.youtube.com/watch?v=ShjXQdKdxsE&index=4&list=PL_Nji0JOuXg0_N0ba-pGABeabXdcfQYtJ>

-Tool utile per creare test bench personalizzati → https://www.doulos.com/knowhow/perl/testbench\_creation/